

LISTING OF CLAIMS AFTER *RESPONSE B*

Appl. No.: 09/992,121

Office action Dated: 04/13/2006

Response Dated: 10/12/2006

Amendments to the Claims

This listing of claims will replace all prior versions, and listings of claims in the application.

Listing of Claims

1. (~~2ND~~ AMENDED per ~~B~~) A computer-implemented method for dynamic emulation of legacy instructions comprising:
 - accessing said legacy instructions in legacy blocks, ~~each legacy block including a plurality of legacy instructions,~~
 - for each particular legacy instruction in a particular legacy block,
 - translating the particular legacy instruction into one or more particular translated instructions for emulating the particular legacy instruction,
 - organizing the particular translated instructions into one or more particular translated blocks,
 - linking the particular translated blocks into a particular linked group corresponding to said particular legacy block; ~~said linking using a link in each particular translated block to point to a location of the next particular translated block of the particular linked group,~~
 - ~~executing the particular translated instructions in the particular translated blocks by sequentially executing the linked group translated blocks.~~
2. (~~Cancelled~~)
3. (ORIGINAL) The method of Claim 1 wherein said particular translated instructions are stored in a cache and wherein said particular translated instructions are purged from said cache only when all said particular translated instructions of particular translated blocks are also purged from said cache.

RespB_06-10-13_fi.doc		10/13/06-06:49
Atty Doc No: AMDH-08156US0	Page 2 of 12	SC/Serial No. 09/992,121

LISTING OF CLAIMS AFTER *RESPONSE B*

Appl. No.: 09/992,121

Office action Dated: 04/13/2006

Response Dated: 10/12/2006

4. (ORIGINAL) The method of Claim 1 wherein said legacy instructions are for a legacy system having a S/390 architecture.
5. (ORIGINAL) The method of Claim 1 wherein said legacy instructions are object code instructions compiled/assembled for a legacy architecture.
6. (ORIGINAL) The method of Claim 1 wherein said translated instructions are for execution in a RISC architecture.

RespB_06-10-13_fi.doc		10/13/06-06:49
Atty Doc No: AMDH-08156US0	Page 3 of 12	SC/Serial No. 09/992,121

LISTING OF CLAIMS AFTER *RESPONSE B*

Appl. No.: 09/992,121

Office action Dated: 04/13/2006

Response Dated: 10/12/2006

7. (2ND AMENDED per B) A computer-implemented method for dynamic emulation of legacy instructions, where the legacy instructions are compiled/assembled into object code form for a native architecture, where the legacy instructions are executed as guests in the host architecture, where the legacy instructions are translated to translated instructions in the host architecture and the translated instructions are executed in the host architecture concurrently with the translation of the legacy instructions in the host architecture, comprising:

accessing said legacy instructions in legacy blocks of a host system operating with said host architecture, each legacy block including a plurality of legacy instructions,

for each particular legacy instruction in a particular legacy block,

translating the particular legacy instruction into one or more particular translated instructions of the host system for emulating the particular legacy instruction as a guest in said host architecture,

organizing the particular translated instructions into one or more particular translated blocks,

linking the particular translated blocks into a particular linked group corresponding to said particular legacy block; said linking using a link in each particular translated block to point to a location of the next particular translated block of the particular linked group,

executing the particular translated instructions in the particular translated blocks by sequentially executing the linked group translated blocks.